

# 2022 SYMPOSIUM ON VLSI TECHNOLOGY and CIRCUITS

Hilton Hawaiian Village, HONOLULU, Hawaii, USA

Monday – Friday, June 13-17, 2022

(June 13 Short Courses,

June 14-16 Technical Sessions,

June 17 Friday Forum)

**Paper Submission Deadline: 23:59 PST Monday, February 7, 2022**  
For complete conference, author, and registration information, visit:

<http://www.vlsisymposium.org>

## Scope

Starting in 2022, Symposia on Technology and Circuits are merged into one Symposium to maximize synergy across both domains. The Symposium encourages submissions in all areas of VLSI Technology and Circuits, including:

- Advanced CMOS devices and technologies
- Advanced packaging, heterogeneous integration
- Beyond-CMOS technologies and platforms, such as quantum devices and computing systems
- Biomedical devices, circuits and systems
- Data converters
- Device and system reliability
- Devices and accelerators for machine learning
- Device Physics, Characterization and Modelling
- Digital circuits, hardware, signal integrity and IOs
- DTCO and design enablement
- Frequency generation and clock circuits
- Hardware security
- Memory technologies, devices, circuits and architectures
- Power, analog and mixed-signal devices and circuits
- Process technology
- Processors and SoCs
- Sensors, imagers, IoT, MEMS, display circuits
- Wireless and RF devices circuits and systems
- Wireline and optical transceivers; optical interconnect and processors
- **Innovative Systems, designed with FPGAs and COTS components**

## Highlights

The Symposium on Technology and Circuits will be organized as a hybrid event with both live sessions onsite in Hilton Hawaiian Village to enable networking opportunities, and on-demand sessions to allow access to selected talks and panels to those who cannot travel. The 5-day event will include: Plenary Session, Technical sessions. Evening panels, short courses on key VLSI topics, demo session for outstanding papers, Forum session, and SSCS / EDS Women in Engineering & Young Professionals events

## Paper Submission

Prospective authors are invited to submit two-page papers and abstracts using the Symposium's website, [www.vlsisymposium.org](http://www.vlsisymposium.org). Accepted papers will be published as submitted, with no revisions permitted. Authors must follow detailed instructions provided within the "Authors" section of the website, including the Authors' Guide and Pre-publication Policy. Students are encouraged to apply to the prestigious Best Student Paper award.

## Further Information

The Symposium website is the central resource for additional information, including details on paper submissions and Best Student Paper eligibility.

<http://vlsisymposium.org>



VLSI

### Symposium Chairs

Tomas Palacios, MIT  
Brian Ginsburg, Texas Instruments

### Symposium Co-Chairs

Yusuke Oike, Sony Semiconductor Solutions  
Katsura Miyashita, Toshiba Corporation

### Program Chairs

Borivoje Nikolić, University of California, Berkeley  
Gosia Jurczak, Lam Research

### Program Co-Chairs

Takaaki Tsunomara, Tokyo Electron  
Mototsugu Hamada, Tokyo University

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